

Docket No. AUS920030547US1

ABSTRACT OF THE DISCLOSURE

**METHOD AND APPARATUS FOR GENERATING INTERRUPTS FOR
SPECIFIC TYPES OF INSTRUCTIONS**

A method, apparatus, and computer instructions for processing instructions. In response to receiving an instruction for execution in an instruction cache in a processor in the data processing system, a determination is made as to whether the instruction indicates enabling a mode of operation in which interrupts are to be generated. Responsive to receiving a subsequent instruction after receiving the instruction, a determination is made as to whether the instruction is of a certain type. If the mode of operation in which interrupts are to be enabled and the instruction is of the certain type, an interrupt is generated.